

FIGURE 1

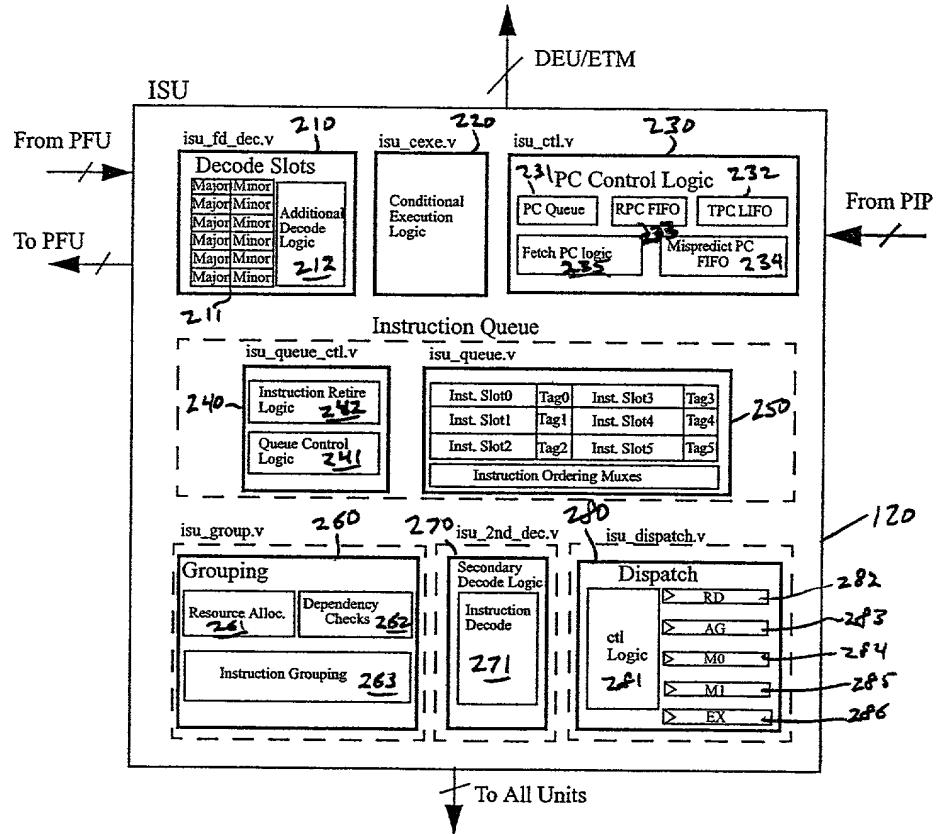


FIGURE 2

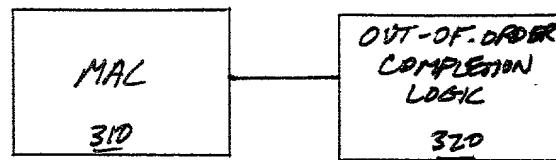


FIGURE 3

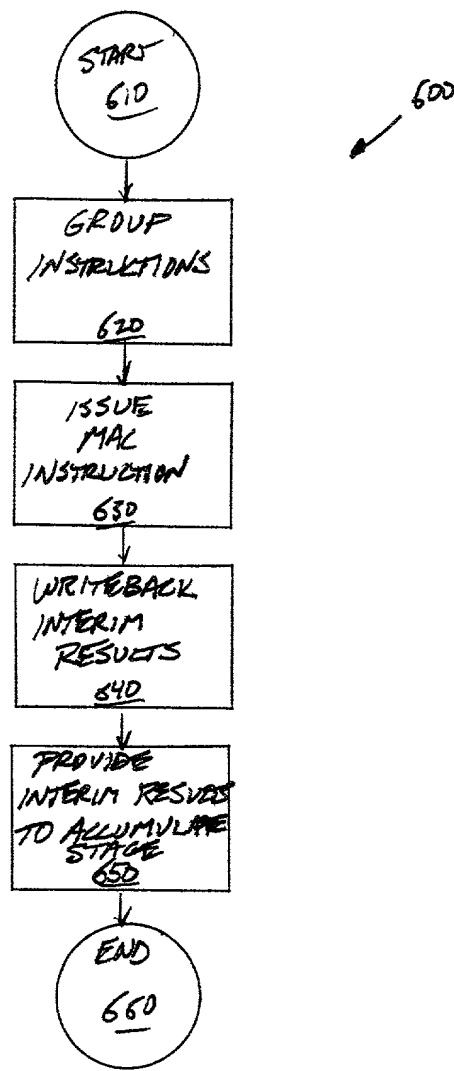


FIGURE 6

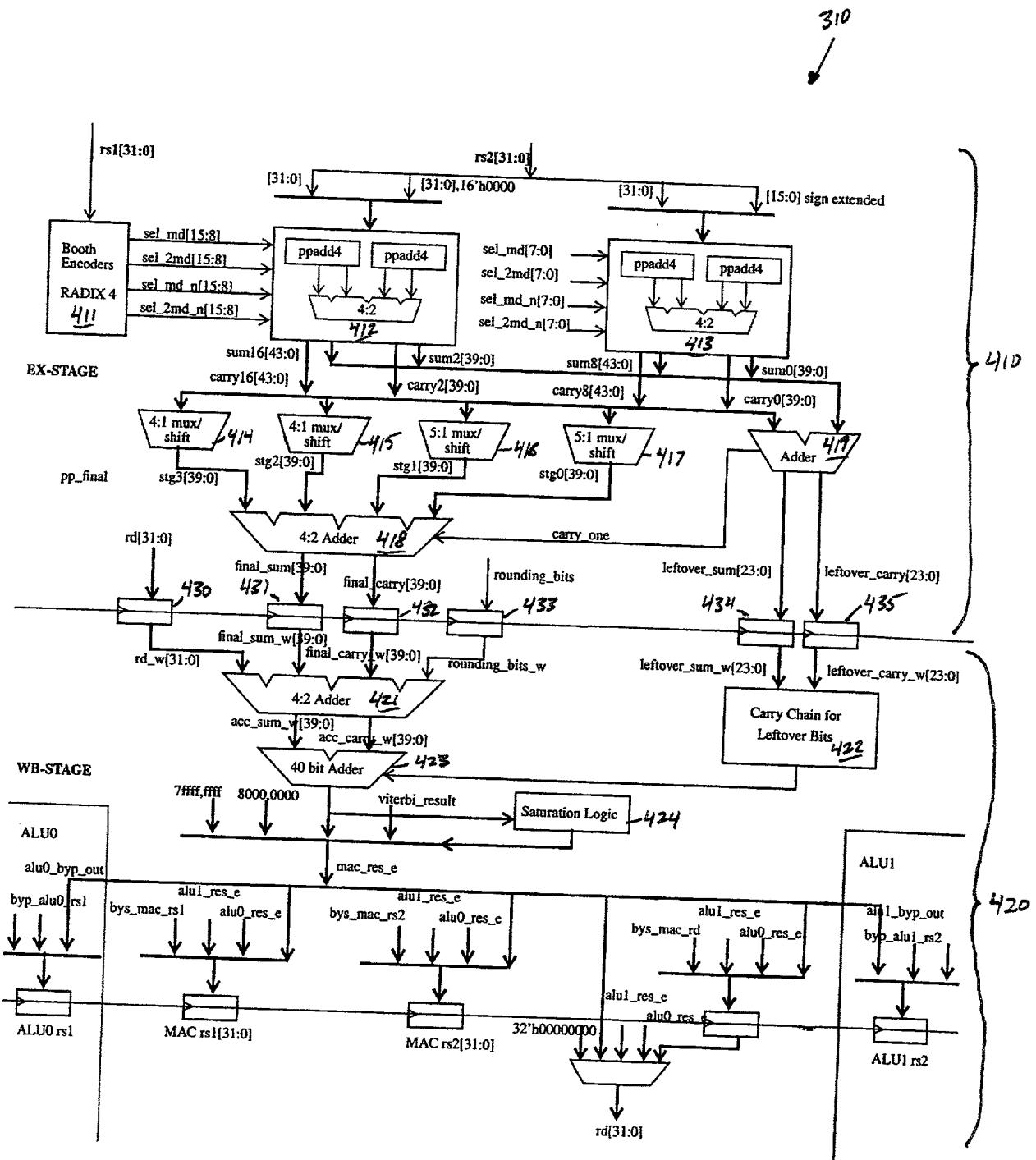


FIGURE 4

loop1:

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ldu    r2, r12, 1 ! r2 = mem[r12], r12=r12+1
ldu    r4, r13, 1 ! r4 = mem[r13], r13=r13+1
mac.a r2, r4      ! {g0 r1 r0} += r2 * r4
agn0  loop1       ! repeat loop1

```

500

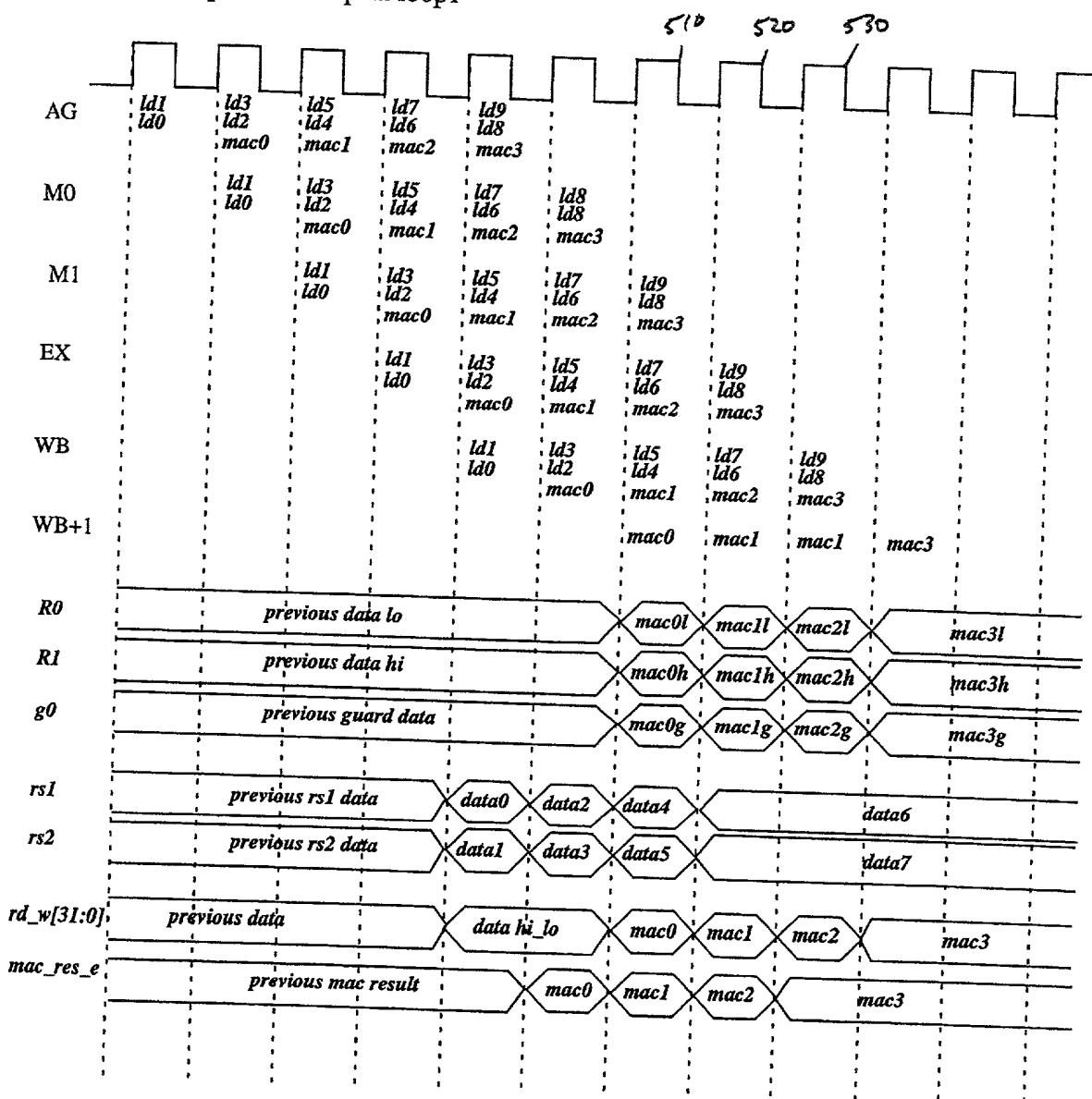


FIGURE 5